

fig1

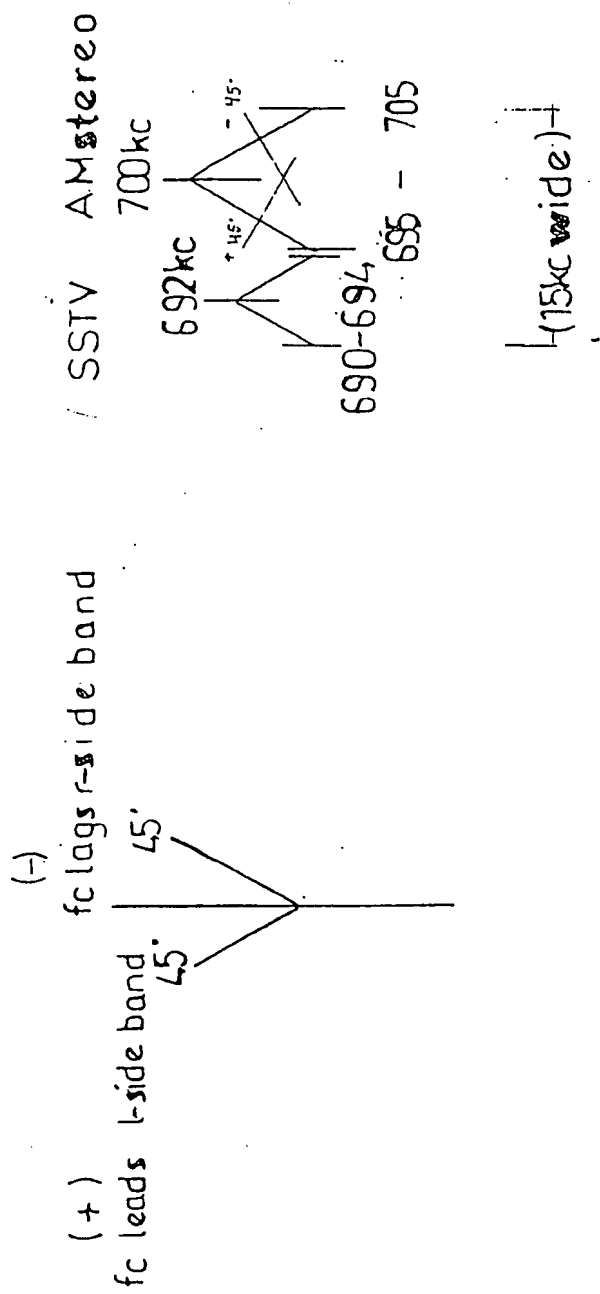
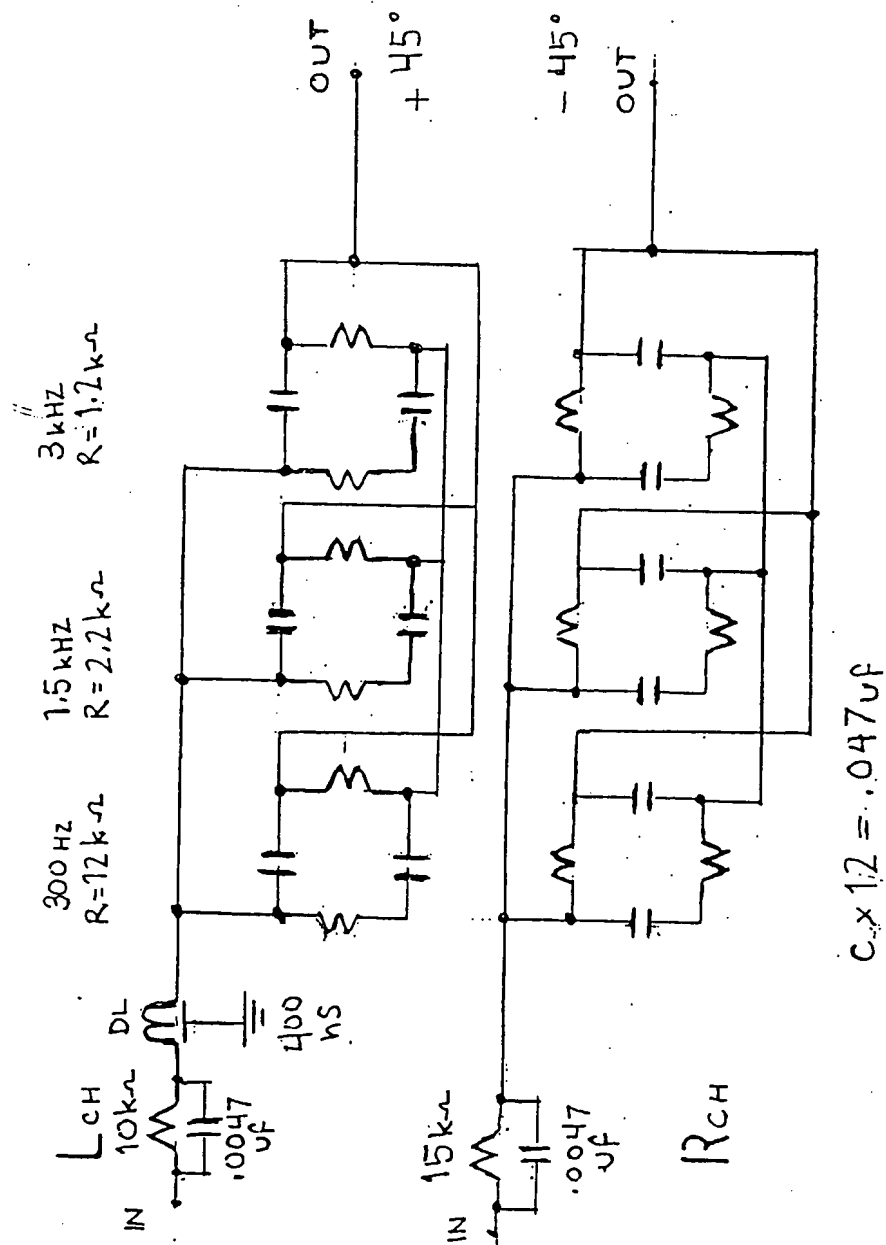
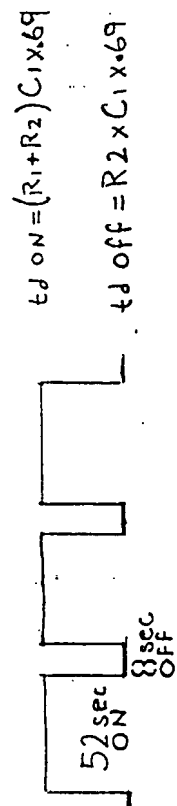
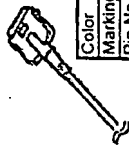


fig 2



Phase Network


$$t_{don} = (R_1 + R_2) C_{1 \times 69}$$
$$t_{\text{doff}} = R2 \times C1 \times 69.$$



# VC-H1 Wire Connections Using Optional Cable E30-3352-08

Color	Orange	Yellow	White	Gray	Pink	Shield
Markings	Red	Black	Red	Black	Red	Black
Pin No.	4	5	6	7	8	9
Pin No.	10	11	12	13	14	15
Pin No.	16	17	18	19	20	21

Note: Pins 1, 2 and 10 are not connected. Pins 3 and 12 are jumpered.

Pin 1 is indicated by a triangle



THIS DATA PART ON VCH1  
SEE SERVICE MANUAL FOR DATA PORT ON VCH1

Wire Color	VC-H1 Pin No.
Orange/Red	4
Shield	16
Yellow/Black	7
Gray/Red	11
Pink/Red	14
White/Black	9
Gray/Black	13

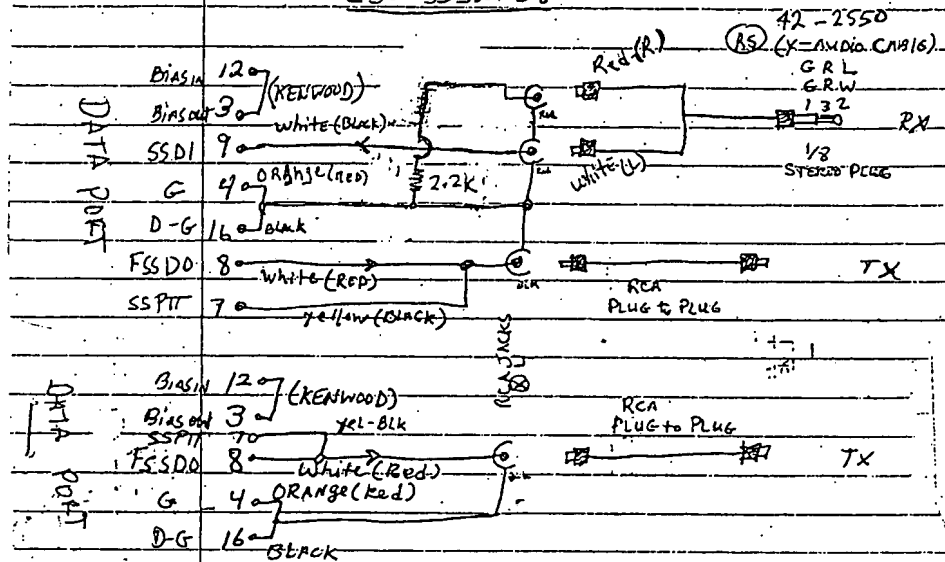
Wire Color	VC-H1 Pin No.
Orange/Red	4
Shield	16
Yellow/Black	7
White/Red	8
White/Black	9

Wire Color	VC-H1 Pin No.
Orange/Red	4
Yellow/Black	7
Yellow/Red	8
White/Black	9
Shield	16

\*\* A 47uf 16v capacitor may be needed to avoid audio distortion  
SERIES ? OR PARALLEL



E30-3352-08



NO, 2 (SSDO) = 9 (FSSDO)

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(6 pages)